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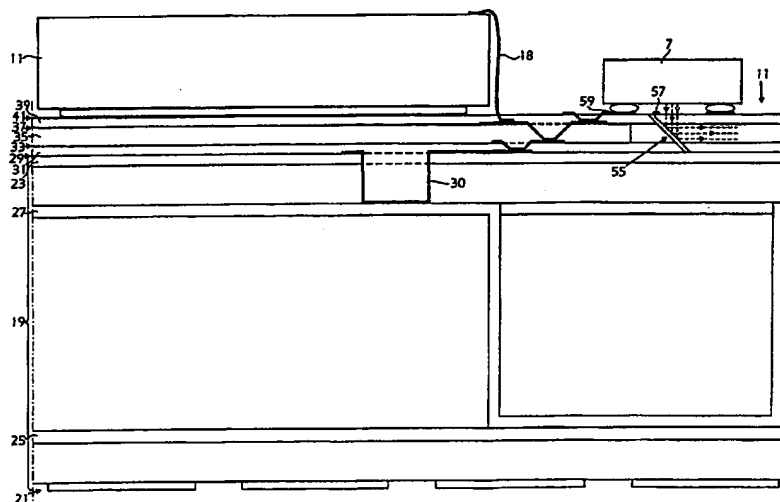


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(54) Title: OPTOELECTRIC MULTICHIP MODULE



(57) Abstract

A thin-film multilayer structure on top of a substrate (19) has three polymer layers (31, 35, 39) having adapted refractive indices in which optical waveguides (9) are formed. Signal conducting metal layers (33, 37, 41) are located at these three thin-film layers. The metal is etched away at the waveguide cores so that ordinary optical waveguides of channel-type are obtained having a refractive index difference between the core and the cladding material. Holes are etched in the polymer layers in order to form electrical vias. Mirrors (55) can be formed by laser ablation to provide connection of an optical waveguide to some component (7) and to provide optical vias, in the case where another similar set of three polymer layers are applied on top of the layers shown. Hence, electrical and optical interconnections can be integrated in the multilayer structure using a minimum number of polymer layers and the optical waveguides can be constructed to have a low loss.

OPTOELECTRIC MULTICHIP MODULE

TECHNICAL FIELD

The present invention relates to optoelectric multichip modules and method of fabricating them using basically polymer materials.

5 BACKGROUND OF THE INVENTION AND PRIOR ART

Telecommunication systems using light propagating in different waveguides expand more and more today. There is a large interest in extending the optical networks even up to private homes and business offices, the so called local access network which is also called "Fibre To (In/From) the Home", "Fibre To (In/From) the Customer (Business)",
10 etc. Also, there is a large interest in extending the use of optical networks in LANs, i.e. local area networks, used for interconnecting computers in a business estate and furthermore for communication inside computer equipment and for communication between computers and peripheral devices such as printers etc. In order to achieve this expansion, the costs of the components of the optical networks of course have to be
15 reduced as much as possible. Very important costs are related to producing the optical transmitter and receiver modules including lasers, LEDs, etc. and other active or passive optical devices.

Only in a few cases attempts have been made to drastically reduce the costs when commercially manufacturing optoelectric and electrooptical modules. For example, the
20 company Motorola has put a concept called "OPTOBUS" on the market. Some details thereof are disclosed in U.S. patent No. 5,659,648 for Knapp et al. In a multilayer structure based on a substrate made of polyimide optical signal layers are used as electrically isolating layers between electrically conducting metal layers. In Figs. 1 and 2 the structures comprise waveguide cores 17, 18, ... and 45, 46 of a polymer material
25 which at their sides have metal strips in a central layer, the metal strips forming part of the waveguide cladding. Layers under and on top of the central layer are made of polyimide and constitute an overcladding and an undercladding. In the structure shown in Fig. 3 whole metal layers 52, 60 are in addition placed between the central layer and the polymer cladding layers. No details are given in regard of positioning electrical signal
30 conductors.

SUMMARY OF THE INVENTION

It is an object of the invention to provide multilayer structures allowing both electrical and optical connections.

It is a further object of the invention to provide multilayer structures allowing
35 electrical interconnections and optical connections having low losses.

It is a further object of the invention to provide a method of fabricating multilayer structures having electrical and optical connections which can be easily executed using a minimum number of processing steps.

A thin-film process is used for sequentially building a multilayer structure on top of

a suitable substrate. In the multilayer structure at least one thin-film layer of a suitable polymer is used both as an electrically isolating layer separating signal conducting metal layers and as a layer of an optical waveguide. The materials of the thin-film structure are selected to be optically transparent to some suitable, selected light wavelength and have adapted refractive indices for this wavelength. Generally, signal conducting metal layers are located between and/or on top of and/or under the three thin-film layers forming the optical waveguide but the metal is etched away at the waveguide cores so that optical waveguides of the type having a refractive index difference between the core and the cladding material are obtained, i.e. the claddings are of a transparent optical material and are formed by portions of the bottom and top layers of the thin-film structure. Hence, electrical and optical interconnections can be integrated in the multilayer structure using a minimum number of polymer layers and the optical waveguides can be constructed having no metal layers for defining them or no metal layers in the direct vicinity of the waveguide cores. Various components can be mounted at the multilayer structure, such as lasers, photodiodes, electronic driver circuits for the optical devices, electronic logical and memory circuits. The components can e.g. be flip-chip mounted or wire bonded. For example a combined cable of ribbon type can be formed, accommodating both electrical conductors and optical waveguides.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will now be described in detail by way of non-limiting embodiments with reference to the accompanying drawings in which:

Fig. 1 is a plan view of a portion of a substrate covered by a multilayer structure forming electrical signal paths and optical waveguides, the portion in particular illustrating a transmitter module and a receiver module having connectors,

Fig. 2 is a plan view in a larger scale showing in particular optical waveguides and the mounting of a laser chip,

Fig. 3 is a cross-sectional view taken perpendicularly to optical waveguides showing the construction thereof,

Fig. 4 is a cross-sectional view showing the mounting of a surface-emitting laser chip and its connection to electrical signal paths and to an optical waveguide,

Fig. 5 is a perspective view illustrating the mounting of a connector device on a substrate,

Fig. 6 is a cross-sectional view showing the mounting of a surface-emitting laser chip and its connection to electrical signal paths and to an optical waveguide, and

Fig. 7 is plan view similar to Fig. 1 illustrating an alternative embodiment of connectors for the transmitter and receiver modules.

DESCRIPTION OF PREFERRED EMBODIMENTS

The fabrication of the multichip module to be described is generally based on the use of materials as described in M. Robertsson, A. Dabek, G. Gustafsson, O.-J. Hagel,

M. Popall, "New Patternable Dielectric and Optical Materials for MCM-L/D- and o/e-MCM-packaging", First IEEE Int. Symp. on Polymeric Electronics packaging, Oct. 26-30, 1997, Norrköping, Sweden. There, photo-patternable polymer materials, ORMOCER™, are disclosed which are suitable for optoelectrical multichip modules in order to build optical waveguides. In particular the refractive indices of these materials can be varied for producing cores and claddings of optical waveguide structures. In addition the materials can be processed at relatively low temperatures of 120 - 180°. Also, the materials have good etching characteristics. The low processing temperatures result in that low-cost substrates not suited for high temperature processing can also be used, so that substrates such as standard materials used for circuit boards, FR4-epoxy, BT-laminates, silicon wafers, ceramics, glass, metal, thin foils of polymers and other materials can be used.

In Fig. 1 a plan view of a portion of a substrate coated with various layers is shown, having optical and electric components mounted thereon. After manufacturing the substrate and having components mounted thereon the substrate is intended to be divided in square modules 1, the division lines being indicated at 3 and marks for cutting the substrate being shown at 5. At 7 a surface-emitting laser chip is shown, comprising five individual laser units. The laser units emit light into corresponding five optical waveguides 9 extending in parallel to each other and perpendicularly to a division line 3 and beyond this division line into a neighbouring module 1. On the module on which the laser chip 7 is mounted an electronic driver circuit chip 11 is located and furthermore electric contact pads 13 are placed in the margin region of the considered module 1 for wired connection of chip capacitors, not shown. On the neighbouring module, into which the optical waveguides 9 extend, a photodetector chip 15 is located receiving light from the five parallel optical waveguides 9. Three electronic driver chips 17 are also located on this module and also electric contact pads are provided. The optical and electronic chips 7, 15, 11, 17 can be electrically connected to the substrate by for example the ball grid array method, as is illustrated by the contact pads 18 drawn in dashed lines.

In the partial elevational view of Fig. 4, the different layers which form the optical waveguides 9 and are located on top of the very substrate 19 are shown, and also electrically conducting layers and interconnections between layers at different levels. A substrate 19 of standard multilayer type is used having metal layers 21, 23 on its bottom and top surfaces respectively and two interior metal layers 25, 27. These metal layers can all be patterned to form suitable electrical conductors paths between various parts. Also, vias can be formed interconnecting the metal layers 21 - 27 of the substrate at suitable places in the conventional way. On top of the upper metal layer 23 of the substrate 19 is a first patterned thin metal layer 29 applied forming vias 30 for contacting the top interior thick metal layer 27 in the substrate 19. On top of the thin metal layer 29 a first polymer layer 31 is applied and on top of this layer in turn a sequence of a second thin,

electrically conducting metal layer 33 for signal transmission, a second polymer layer 35, a third electrically conducting, thin metal layer 37 for signal transmission, a third polymer layer 39, a fourth electrically conducting, thin metal layer 41 for signal transmission is located. The driver circuit 11 is here as an alternative shown to be
5 connected by wires 42 to contact areas of the third metal layer 37.

The polymer layers 31, 35, 39 are made of the polymer ORMOCER as described in the article cited above. The polymer layers 31, 35, 39 have adapted refraction indices in order to be capable of forming an undercladding, a waveguide core and an overcladding of optical waveguides as will be described hereinafter. Typical thicknesses are 5 - 20 μm ,
10 e.g. 10 μm , for the first or bottom and the third or top polymer layers 31, 39 and 5 - 70 μm , e.g. 20 μm , for the second or intermediate polymer layer 35. These polymer layers can all be patterned but they will only comprise a very small total area of holes or cut-outs, primarily only via holes for allowing electrical interconnections between different levels. The second polymer layer 37 is in addition patterned to allow that cladding
15 portions are formed at the sides of the waveguide cores formed in this layer, as will be described hereinafter. The top polymer layers 35, 37 can be patterned to comprise long parallel grooves 43 for allowing alignment of optical connectors 44 intended to be connected to one or two modules 1, see Fig. 1. Also, the top polymer layer 39 can have cut-outs to allow electrical contacting of the third electrical signal layer 37 from the top
20 side of the structure with contact pads 13 provided in that metal layer.

The electrical signal layers 29, 33, 37, 41 are all very thin and can have a thickness of typically 3 μm to be compared to the thicknesses of the substrate metal layers 21, 23, 25, 27 which can be of the order of 200 μm . The three inner layers 29, 33, 37 in the top multilayer structure can be made of aluminium by sputtering. The top layer 41 is made of
25 a sequence of layers comprising undermost a sputtered layer of aluminium, thereon a very thin titanium layer and a thin copper layer and on top a thicker nickel-layer coated with a thin gold layer. They are all patterned to form conductor paths and possibly electric contact pads and to fill via holes in the underlying polymer layer for contacting the electrically conducting layer located next thereunder. Rather little metal material may
30 be left in each electrical signal layer and in particular there is no metal material at the bottom and top surfaces of the third polymer layer 39 at the areas in which it forms optical waveguide cores in order not to interfere with the propagation of light in the waveguides and allow a straight extension and a uniform cross-section of the waveguide cores.

35 In the enlarged view in Fig. 2 the top metal layer and the top polymer layer are shown and in particular the area under and at the laser chip 7. The waveguides 9 are here seen to comprise waveguide cores 51 formed of strips of the second polymer layer 35. At the sides of the cores 51 in this layer grooves 53 have been formed which then have been filled with material from the next polymer layer, the third or top polymer layer 39, see

also the cross-sectional view of Fig. 3. The grooves 53 can have a width corresponding to the thickness of the overcladding and undercladding layers, i.e. having a width of e.g. 10 μm . The grooves 53 and thus the waveguides 9 extend under the laser chip 7, and there, in each waveguide, a mirror 55 is formed by a deep, oblique recess produced by laser ablating from the top of the layer assembly, see also Fig. 4. At least one edge of the mirror recesses 55 at the surface of the layer assembly is limited by strips 57 of metal of the top metal layer 41, the appropriate side of these metal strips 57 defining the position of the respective mirror recess 55. Contact pads 59 for electrically contacting the laser chip 7 and for aligning it by the use of surface tension forces when soldering the laser chip are also formed by the top metal layer 41. The contact and aligning pads 59 and the mirror defining strips 57 are thus formed by the same metal layer and using the same mask step for patterning this metal layer.

In the lower portion of Fig. 1 and in particular in Fig. 5 a connector structure 44 is visible. It is intended to form connectors of a kind as described in Swedish patent application 9504549-8. The connector structure 44 has the shape of an elongated rectangular plate from which, at the long sides thereof, ribs 61 project downwards. The ribs are symmetrically located at the long sides and end at some distance from the short sides of the rectangular connector body. The ribs 61 are intended to approximately position the connector structure by placing them in cut-out grooves 63 passing all through the polymer layers and the substrate 19. The grooves are machined for example after applying all the metal and polymer layers but before mounting components. For a fine positioning or alignment of the connector structure 44 it has also low alignment ribs, not visible, placed on the underside of the connector structure plate portion between the high ribs 61. The low alignment ribs cooperate with the grooves 43 in the top polymer layers in the structure as described above.

The connector structure 44 bridges two neighbouring modules 1. It is located above waveguides 9 extending between the modules and is intended to form aligning connectors for the waveguides. The connector structure 44 is mounted at the same time as other components are mounted on the coated substrate 19 and may e.g. be attached to the surface of the substrate by glue pads 67, see Fig. 1. In the embodiment illustrated in Figs. 1 and 5 end portions of the plate-shaped body of the connector structure extend over the laser chip 7 and the photodetector chip 15 to form a protection thereof. After mounting all components and connector structures the substrate 19 is split into individual modules 1, by e.g. sawing, along the lines 3 as defined by the marks 5. After such a sawing operation the waveguides 9 are also cut off to have ends at the module borders. The ends of the waveguides will then be located in the same perpendicular or vertical plane as the cut-off ends of the connector structure 44, which by the splitting operation is divided into two connectors, one on each module 1.

Attaching the connector structures 44 before splitting the substrate into modules can

lower the manufacturing costs, both by attaching basically two connectors in one operation and by having the end surfaces of the individual connectors located in the same cut-off plane as the end surfaces of the waveguides, which facilitates polishing the end surfaces which is necessary in order to form well-aligned waveguide connections having a
5 low attenuation.

In the surface structure allowing waveguides to be formed of course also edge emitting lasers can be used. This is illustrated in the sectional view of Fig. 6. Here an edge-emitting laser unit 7' is mounted in a recess 71 made in the two top polymer layers 35 and 39. The laser unit comprises a plurality of individual lasers issuing light into
10 respective waveguides 11. The laser unit 7' can be protected by an additional polymer layer 73. This layer 73 can also penetrate between the surface of the laser unit 71 and the opposite end surfaces of the waveguides 11. It is then advantageously given a suitable refractive index in order to match the refractive indices of the laser units and the waveguides in order to lower the attenuation of light coupled from the laser units into the
15 waveguides 11 and to reduce back reflection of light into the laser units.

An alternative embodiment of the connector structure is illustrated in Fig. 7. The connector structure 44' is there illustrated to cover the main portions of two adjacent multilayer modules and thus all the components of each module. The cut-out grooves 63' are located at two opposite edges of each module and the parallel low ribs 43' extend
20 next to the grooves. The low ribs can be designed as a continuous rib parallel to three edges of each module for sealing each module at these three edges in addition to their main function of accurately positioning the connector structure 44' and thereby the individual connectors formed when splitting the substrate.

CLAIMS

1. A multilayer structure for conducting electrical and optical signals, comprising
a first polymer layer forming an undercladding,
a second polymer layer placed on top of the first polymer layer and comprising at
5 least one waveguide core, and
a third polymer layer placed on top of the second polymer layer and forming an
overcladding,
characterized by two patterned metal layers located at the opposite surfaces of one
of the polymer layers and forming electrical signal conductors, this polymer layer being
10 electrically isolating, the two metal layers being patterned so that at the at least one
waveguide core, in one of the two metal layers which is located at the second polymer
layer, the metal material is removed in at least one strip at and parallel to the at least one
waveguide core, the at least one waveguide core thereby only being bounded or
surrounded by material in the polymer layers.
- 15 2. A multilayer structure according to claim 1, characterized in that the second
polymer layer is patterned to form strip-shaped recesses at the sides of the waveguide
cores, in which strip-shaped recesses material from the third polymer layer is located and
forms side claddings.
3. A multilayer structure according to claim 2, characterized in that the width of
20 the strip-shaped recesses at the sides of the waveguide cores and the thicknesses of the
first and third polymer layer are substantially equal to each other.
4. A multilayer structure according to any of claims 1 - 3, characterized by a
substrate having electrical signal paths in the substrate, the polymer layers being applied
on a surface of the substrate and the multilayer structure further including a plurality of
25 individual multilayer modules, in which the multilayer structure is intended to be divided,
at least one waveguide core extending between two neighbouring multilayer modules to
form an optical waveguide, which correspondingly extends between the two neighbouring
multilayer modules, so that the optical waveguide when dividing the multilayer structure
in modules is divided in two portions, which each one is located on a separate module.
- 30 5. A multilayer structure according to claim 4, characterized by a connector
structure mounted on the free surface of the polymer layers at said optical waveguide
intended to be divided in two portions, and on the two neighbouring multilayer modules,
whereby, when dividing the multilayer structure in modules, the connector structure is
divided in two portions, each portion of which being a connector for a respective portion
35 of the optical waveguide then also being divided in two portions.
6. A multilayer structure according to claim 5, characterized in that the connector
structure is mounted to cover the main portions of the two neighbouring multilayer
modules, so that, when dividing the multilayer structure in modules, the produced
modules will have their corresponding connector portions of the connector structure form

an encapsulation of the module and of possible component mounted on the module.

7. A multilayer structure according to any of claims 1 - 6, characterized by a recess in the polymer layers, the recess being located so that an optical waveguide has an end surface in a side wall surface of the recess, and an optical component mounted in the
5 recess for coupling optical signals to and/or from the optical component from and/or to the optical waveguide respectively.

8. A multilayer structure for conducting electrical and optical signals, characterized by a sequence of layers comprising

a first patterned metal layer,

10 an electrically isolating first polymer layer forming an undercladding,

a second patterned metal layer,

an electrically isolating second polymer layer comprising waveguide cores and at the sides of the waveguide cores strips forming side claddings and having a lower refractive index than that of the waveguide cores,

15 a third patterned metal layer, and

an electrically isolating third polymer layer forming an overcladding.

9. A multilayer structure for conducting optical signals, characterized by a sequence of layers comprising

an electrically isolating first polymer layer forming an undercladding,

20 an electrically isolating second polymer layer comprising waveguide cores and at the sides of the waveguide cores narrow strips in which side claddings are formed and which have a lower refractive index than that of the waveguide cores, and

an electrically isolating third polymer layer forming an overcladding,

the width of the narrow strips and the thicknesses of the undercladding and
25 overcladding layers being substantially equal to each other.

10. A multilayer structure according to any of claims 1 - 9, characterized in that the first and third polymer layers are substantially whole layers extending all over the multilayer structure, only having via holes for electrical and optical connections between different layers and to the exterior.

30 11. A multilayer structure according to any of claims 1 - 10, characterized by optical and/or electrical components mounted to a surface of the multilayer structure, an additional polymer layer covering at least part of the surface of the multilayer structure and the components.

12. A multilayer structure according to claim 11, characterized in that the
35 additional polymer layer has an adapted refractive index selected to improve the optical coupling between components and waveguides and penetrates into at least one space or slot between a surface of a component, at which surface the component is arranged to emit or to receive optical signals, and an end surface of an optical waveguide.

13. A multilayer structure according to any of claims 1 - 12, characterized by at

least two sequences of polymer layers, one of the sequences being formed on top of another one of the sequences, and optical waveguides located in each sequence.

14. A multilayer structure for conducting electrical and optical signals, the multilayer structure comprising

5 a first polymer layer forming an undercladding,

a second polymer layer located on top of the first polymer layer and comprising at least one waveguide core, and

a third polymer layer located on top of the second polymer layer and forming an overcladding,

10 so that in the polymer layers at least one optical waveguide is formed,

characterized by a substrate having electrical signal paths in the substrate, the polymer layers being applied on a surface of the substrate and further the multilayer structure including a multitude of individual multilayer modules, in which the multilayer structure is intended to be divided, at least one waveguide core extending between two
15 neighbouring multilayer modules to form an optical waveguide, which correspondingly extends between the two neighbouring multilayer modules, so that the optical waveguide when dividing the multilayer structure in modules is divided in two portions, which each one is located on a separate module.

15. A multilayer structure according to claim 14, characterized by a connector
20 structure mounted on the surface of the polymer layers at said optical waveguide intended to be divided in two portions, and on the two neighbouring multilayer modules, whereby, when dividing the multilayer structure in modules, the connector structure is divided in two portions, each portion of which being a connector for a respective portion of the optical waveguide then also being divided in two portions.

25 16. A multilayer structure for conducting electrical and optical signals, the multilayer structure comprising

a first polymer layer forming an undercladding,

a second polymer layer located on top of the first polymer layer and comprising at least one waveguide core, and

30 a third polymer layer located on top of the second polymer layer and forming an overcladding,

so that in the polymer layers at least one optical waveguide is formed,

characterized by a recess in the polymer layers, the recess being located so that an optical waveguide has an end surface in a side wall surface of the recess, and an optical
35 component mounted in the recess for coupling optical signals to and/or from the optical component from and/or to the optical waveguide respectively.

17. A method of producing multilayer modules for conducting electrical and optical signals, comprising the steps of
providing a substrate,

applying on the substrate a first polymer layer to form an undercladding,
applying on top of the first polymer layer a second polymer layer to form at least
one waveguide core, and

applying on top of the second polymer layer a third polymer layer to form an
5 overcladding,

characterized by applying two metal layers, one of the metal layers at a surface of
one of the polymer layers which is electrically isolating and another one of the metal
layers at an opposite surface of said one of the polymer layers, and patterning the two
metal layers to form electrical signal conductors, the patterning being made so that at the
10 at least one waveguide core, in one of the two metal layers which is located at the second
polymer layer, the metal in the metal layer is removed in a strip at and parallel to the at
least one waveguide core, so that the at least one waveguide core is only bounded or
surrounded by material in the first, second and third polymer layers.

18. A method according to claim 17, characterized by the additional step of
15 patterning, before applying the third polymer layer, the second polymer layer to form
strips at sides of the at least one waveguide core, whereby, when applying the third
polymer layer, material from the third polymer layer penetrates into the strips to form
side claddings of the at least one waveguide core.

19. A method according to claim 18, characterized in that in the step of patterning
20 the second polymer layer the strips are given a width substantially equal to the
thicknesses of the first and third polymer layers.

20. A method according to any of claims 17 - 19, characterized by the additional
step of dividing the substrate into a plurality of individual multilayer modules, the
dividing being made to divide at least one optical waveguide in two portions, which each
25 one is located on an individual multilayer module.

21. A method according to claim 20, characterized by the additional step of
mounting, before the step of dividing the substrate, components on at least one of the
layers and the surface of the substrate.

22. A method according to claim 21, characterized by the additional step of
30 mounting, before the step of dividing the substrate, a connector structure on the surface
of the substrate at said at least one optical waveguide, which in the step of dividing the
substrate is divided in two portions, the mounting of the connector structure being made
in such a way that the connector structure in the step of dividing the substrate is divided
in two portions, each portion of which is a connector for a respective portion of said at
35 least one optical waveguide which is divided in the step of dividing the substrate.

23. A method according to any of claims 17 - 22, characterized by the additional
step of making a recess in the polymer layers and locating the recess so that an optical
waveguide has an end surface in a side wall surface of the recess, and the further
additional step of mounting an optical component in the recess for coupling optical

signals to and/or from the optical component from and/or to the optical waveguide respectively.

24. A method of producing multilayer modules conducting electrical and optical signals, characterized by the sequential steps of

- 5 providing a substrate,
- applying on a surface the substrate a first patterned metal layer,
- applying thereon an electrically isolating first polymer layer to form an undercladding,
- applying thereon a second patterned metal layer,
- 10 applying thereon an electrically isolating second polymer layer comprising waveguide cores and at the sides of the waveguide cores strip-shaped recesses,
- applying thereon a third patterned metal layer, and
- applying thereon an electrically isolating third polymer layer to form an overcladding, material from the third polymer layer then penetrating into the strip-shaped
- 15 recesses to form side claddings.

25. A method of producing multilayer modules conducting electrical and optical signals, characterized by the sequential steps of

- providing a substrate,
- applying thereon an electrically isolating first polymer layer to form an
- 20 undercladding,
- applying thereon an electrically isolating second polymer layer comprising waveguide cores and at the sides of the waveguide cores narrow strip-shaped recesses,
- and
- applying thereon an electrically isolating third polymer layer to form an
- 25 overcladding, material from the third polymer layer then penetrating into the narrow strip-shaped recesses to form side claddings,
- the width of the narrow strips being made to be substantially equal to the thicknesses of the first and third polymer layers.

26. A method according to any of claims 17 - 25, characterized in that in the steps

30 of applying the first and third polymer layers the first and third polymer layers are applied as substantially whole layers extending all over the multilayer structure and only having via holes for electrical and optical connections between different layers and to the exterior.

27. A method according to any of claims 17 - 26, characterized by the additional

35 step of mounting optical and/or electrical components to the surface of the substrate and the further additional step of applying an additional polymer layer to cover at least part of the surface of the multilayer structure and of the components.

28. A method according to claim 27, characterized in that in step of applying the additional polymer layer the additional polymer layer is applied with a refractive index

selected to improve the optical coupling between components and waveguides, the additional polymer layer being made to penetrate into at least one space or slot between a surface of a component, at which surface the component is arranged to emit or to receive optical signals, and an end surface of an optical waveguide.

5 29. A method according to any of claims 17 - 28, characterized by the additional steps of applying, after applying the first, second and third of polymer layers, on top of the structure formed at least one similar sequence of first, second and third polymer layers, so that optical waveguides are formed in each sequence.

30. A method of producing multilayer modules for conducting electrical and optical
10 signals, characterized by the steps of

providing a substrate having electric signal paths in the substrate,

applying layers on a surface of the substrate for forming optical waveguides for conducting optical signals,

splitting the substrate into a plurality of individual multilayer modules, the splitting
15 being made to divide at least one optical waveguide into two portions located on individual modules.

31. A method according to claim 30, characterized by the additional step of mounting, before splitting the substrate, components on at least one of the layers and the surface of the substrate.

20 32. A method according to claim 30, characterized by the additional step of mounting, before splitting the substrate, a connector structure on the surface of the substrate, the connector structure being mounted to be located at said at least one optical waveguide, which is divided into two portions, in such a way that the connector structure in the step of splitting is divided into two portions, each portion of which being a
25 connector of the portion of said at least one optical waveguide which is divided in the step of splitting the substrate.

33. A method of producing multilayer structures for conducting electrical and optical signals, characterized by the steps of

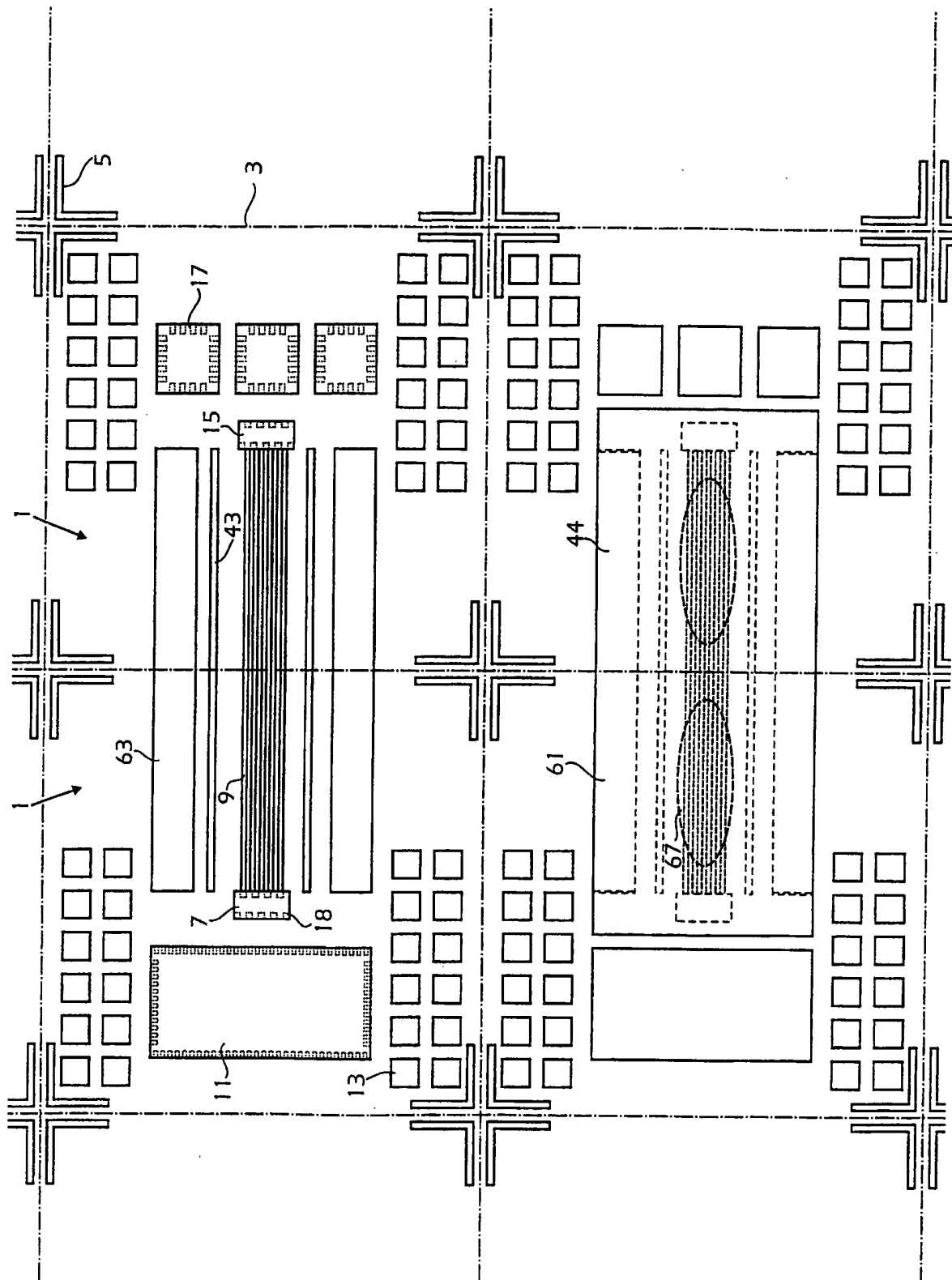
providing a substrate having electric signal paths therein,

30 applying layers on a surface of the substrate to form optical waveguides for conducting optical signals,

etching thereupon a recess into the layers at a location so that an optical waveguide has an end surface in a side wall surface of the recess, and

mounting an optical component in the recess for coupling optical signals to and/or
35 from the component from and/or into the optical waveguide respectively.

Fig. 1



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Fig. 2

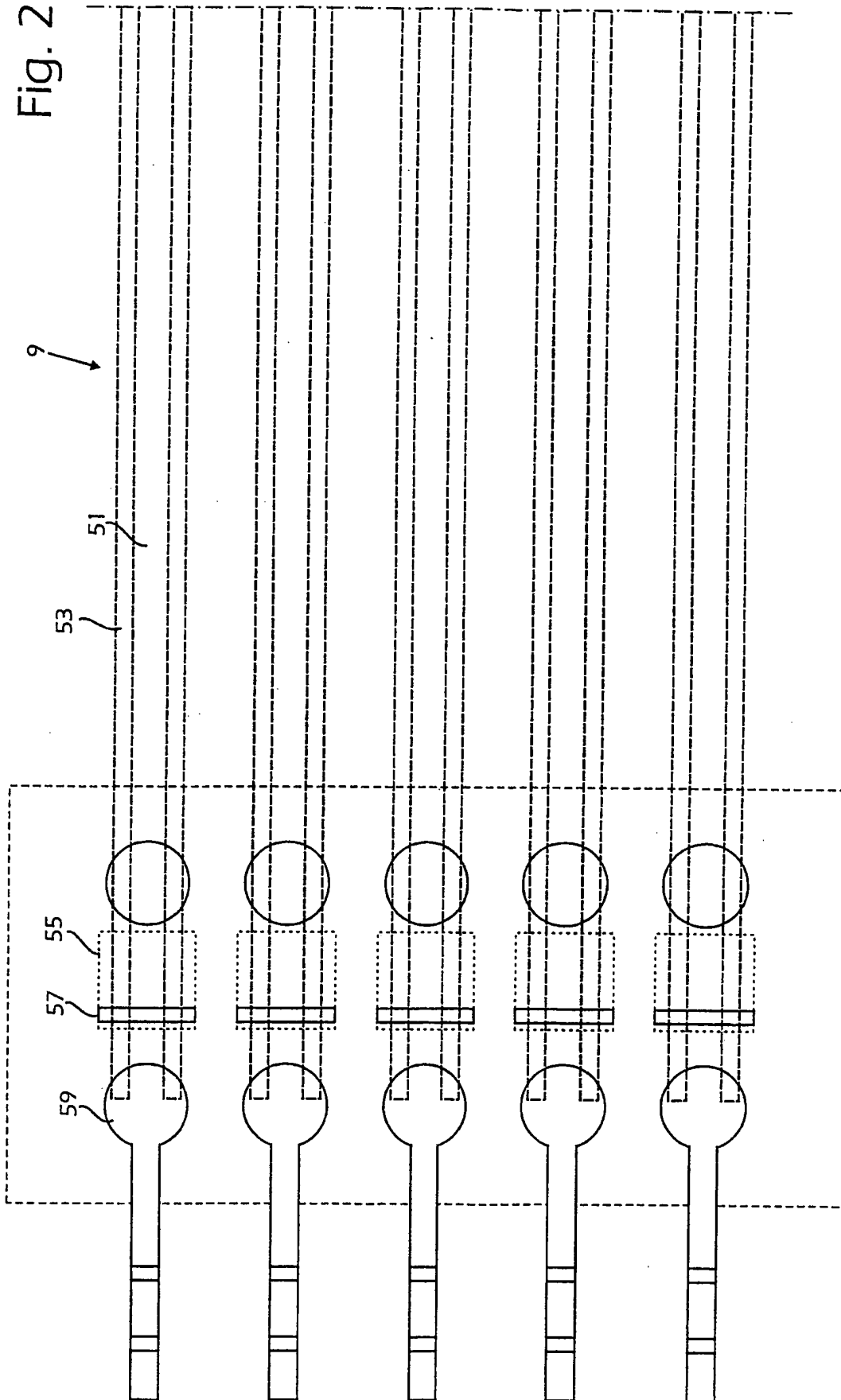


Fig. 3

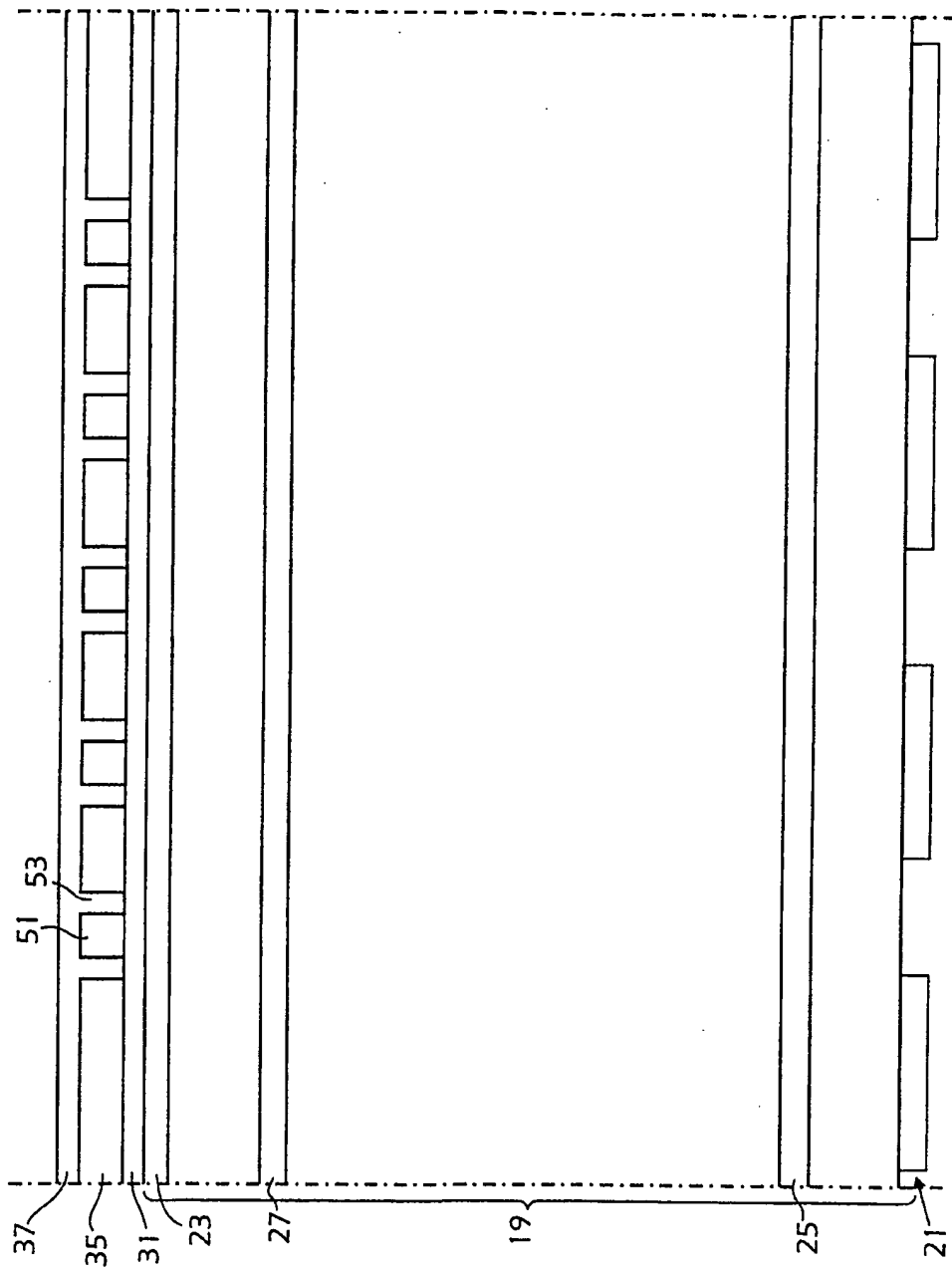
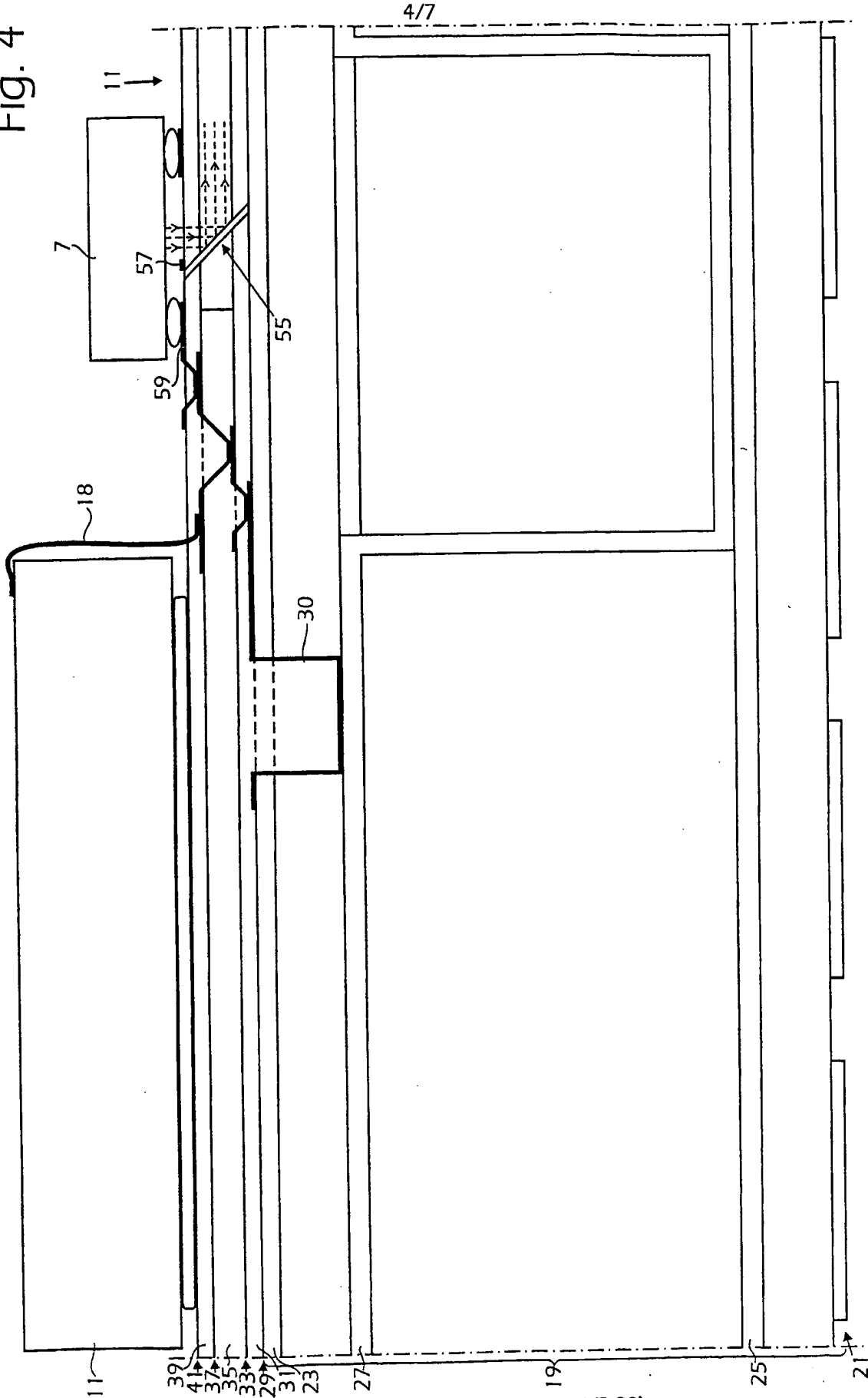
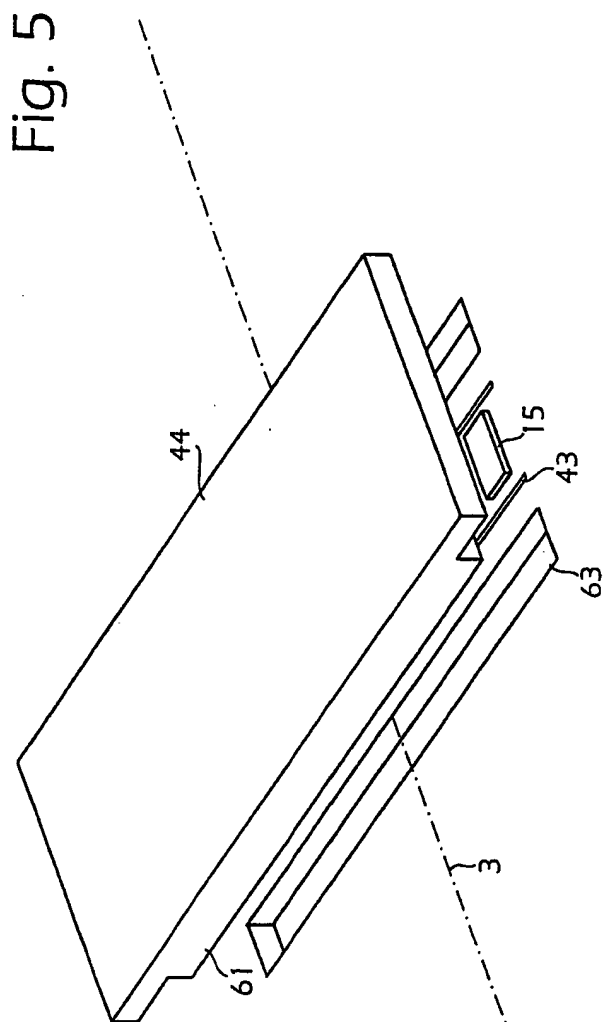


Fig. 4





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Fig. 6

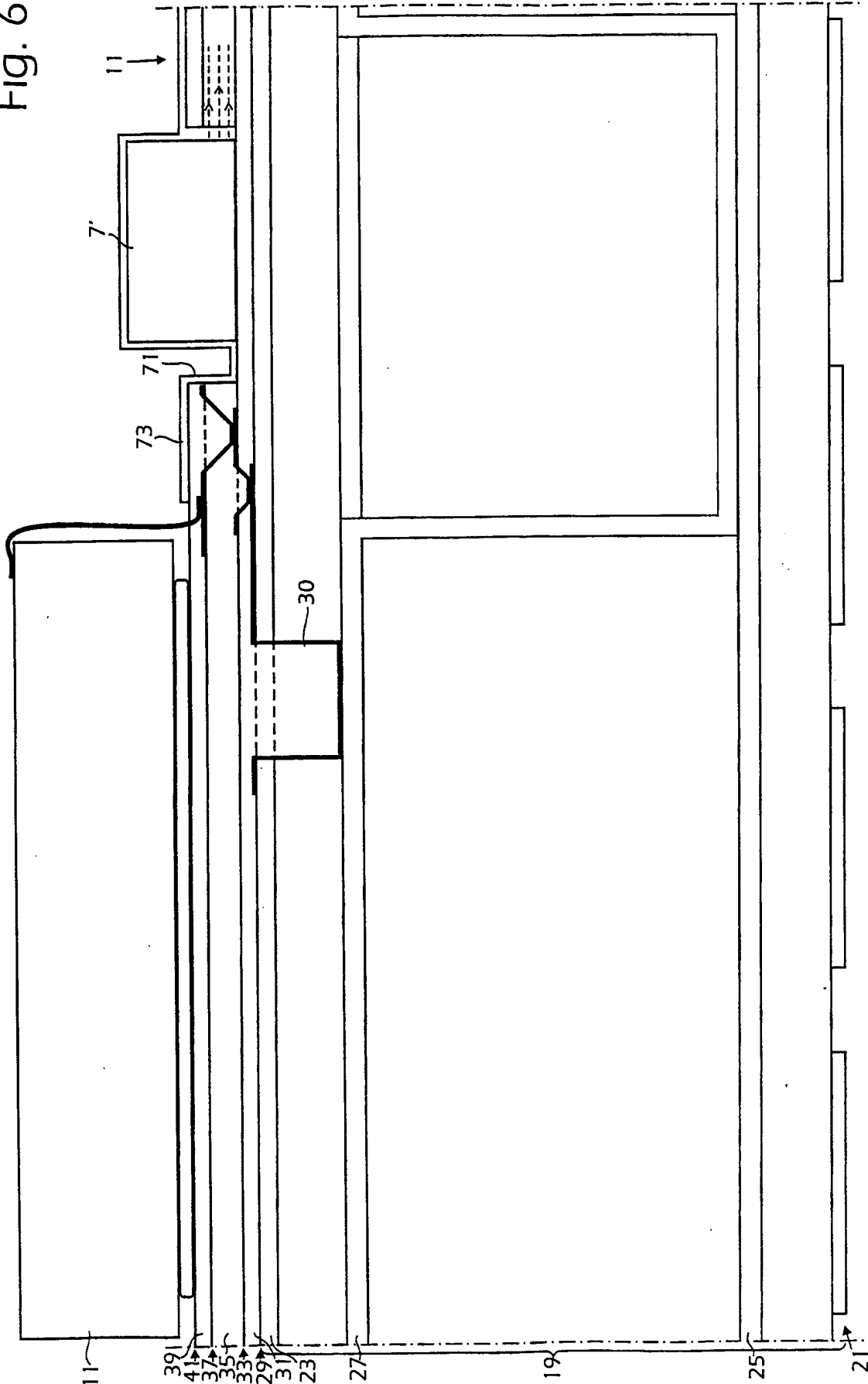


Fig. 7

